



1200V,40A,Trench field-stop , IGBT

TO-247-3L (\*Prefix :W)

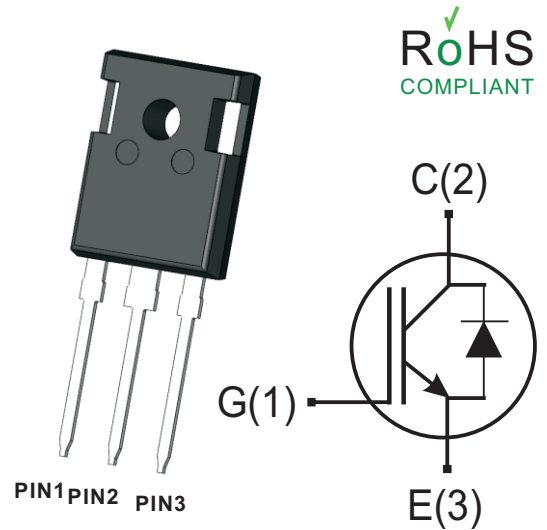
V <sub>CES</sub>	1200V
I <sub>c</sub> (T <sub>c</sub> 100°C)	40A
V <sub>CE(sat)</sub> (typ)	1.7V

DESCRIPTION

- Positive temperature coefficient
- Low V<sub>CEsat</sub>
- Low saturation voltage
- High switching frequency
- Easy paralleling
- Rohs Compliant

APPLICATIONS

- Motor drives
- Inverters
- Uninterruptible Power Supplies
- Converters



ROHS  
COMPLIANT

SYMBOL

ABSOLUTE MAXIMUM RATINGS (TA=25°C, unless otherwise specified)

PARAMETER	Symbols	RATINGS	Units
Collector-emitter voltage	V <sub>CES</sub>	1200	V
Gate-emitter voltage	V <sub>GES</sub>	±20	V
Continuous Drain Current	I <sub>c</sub>	T <sub>c</sub> =25°C	80
		T <sub>c</sub> =100°C	40
Pulsed Drain Current	I <sub>cm</sub>	160	A
Diode Forward Current	I <sub>F</sub>	40	A
Power Dissipation (T <sub>c</sub> = 25°C)	P <sub>D</sub>	416	W
Operating junction temperature	T <sub>j</sub>	-55 ~ +150	°C
storage temperature	T <sub>stg</sub>	-55 ~ +150	°C

Thermal Resistance

PARAMETER	Symbols	RATINGS	Units
Thermal resistance IGBT junction – case.	R <sub>thJC</sub>	0.3	°C/W
Thermal resistance Diode junction – case.	R <sub>thJC</sub>	0.4	°C/W
Thermal resistance, junction – ambient	R <sub>thJA</sub>	40	°C/W



**Electrical Characteristics of the IGBT (Tj= 25°C unless otherwise specified)**

PARAMETER	Symbols	TEST CONDITIONS	Min	Typ	Max	Units
<b>Static</b>						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE}=0V, I_{CE}=0.25mA$	1200			V
Zero gate voltage collector current	$I_{CES}$	$V_{CE}=1200V, V_{GE}=0V$			100	$\mu A$
Gate-emitter leakage current	$I_{GES}$	$V_{GE}=\pm 20V, V_{CE}=0V$			$\pm 100$	nA
Collector-emitter saturation voltage	$V_{CE(sat)}$	$V_{GE}=15V, I_C=40A$		1.7	2.0	V
Gate-emitter threshold voltage	$V_{GE(TH)}$	$V_{GE}=V_{CE}, I_C=1mA$	5.4	5.7	6.0	V
<b>Dynamic</b>						
Input Capacitance	$C_{ies}$	$V_{CE}=30V,$		3230		$\mu F$
Output Capacitance	$C_{oes}$	$V_{GE}=0V,$		198		$\mu F$
Reverse Transfer Capacitance	$C_{res}$	$f=1.0MHz$		29		$\mu F$
Gate resistance	$R_G$	$V_{DS}=0V, F_{REQ}=1.00MHz$		2.6		$\Omega$

**Electrical Characteristics of the Diode (Tj= 25°C unless otherwise specified)**

PARAMETER	Symbols	TEST CONDITIONS	Min	Typ	Max	Units
<b>Static</b>						
Diode Forward Voltage	$V_F$	$V_{GE}=0V, I_F=40A$		1.7	2.05	V
<b>Dynamic</b>						
Diode reverse recovery time	$TRR$	$I_F=0.5A, I_R=1.0A$ $I_{rr}=0.25A$			85	ns

**Switching Characteristic, Inductive Load**

PARAMETER	Symbols	TEST CONDITIONS	Min	Typ	Max	Units
<b>Dynamic</b>						
Turn-on Delay Time	$t_{d(on)}$	$V_{CC}=600V, I_C=40A,$		40		ns
Rise Time	$t_r$	$V_{GE}=15V, R_g=10\Omega$		89		ns
Turn-on Energy	$E_{on}$	$T_j=25^\circ C$		4.0		mJ
Turn-off Delay Time	$t_{d(off)}$			380		ns
Fall Time	$t_f$			47		ns
Turn-off Energy	$E_{off}$			1.8		mJ



### Typical Characteristics

Fig.1 Typical output characteristic

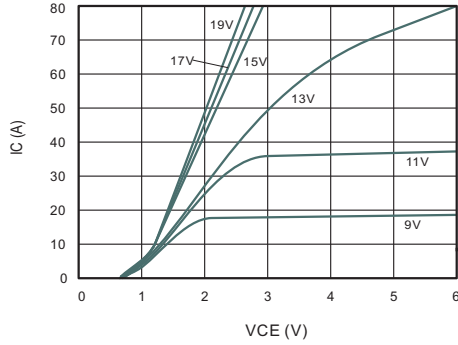


Fig.2 Capacitance Characteristics

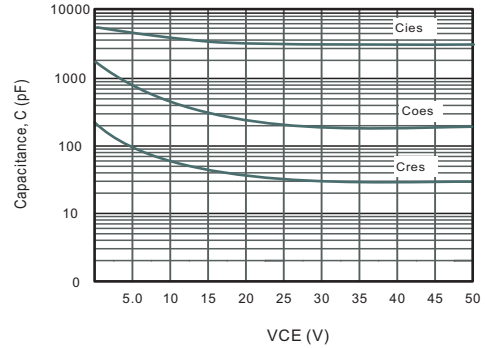


Fig.3 Power Dissipation

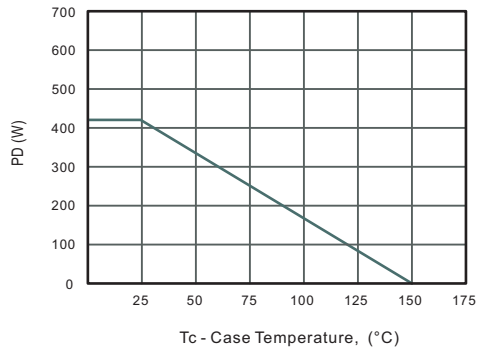


Fig.4 Collector Current Derating

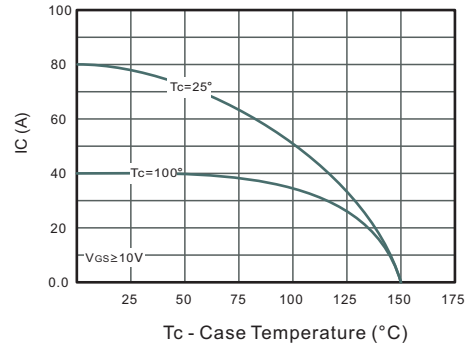


Fig.5 Typical  $V_{GE(th)}$  as a function of  $T_J$

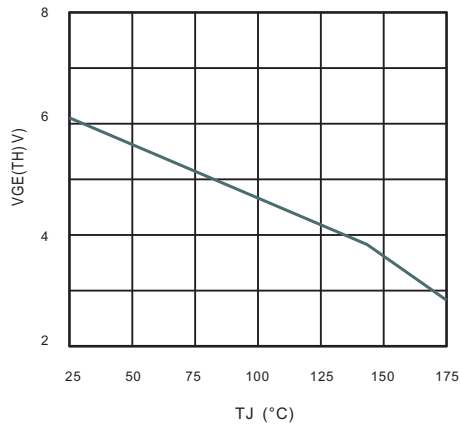


Fig.6 Drain-Source On-Resistance vs. Drain Current

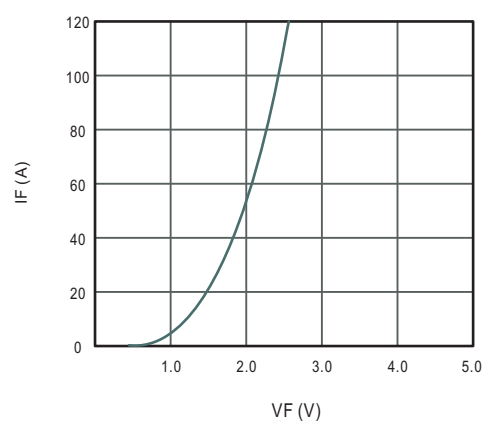




Fig.7 Safe Operating Area

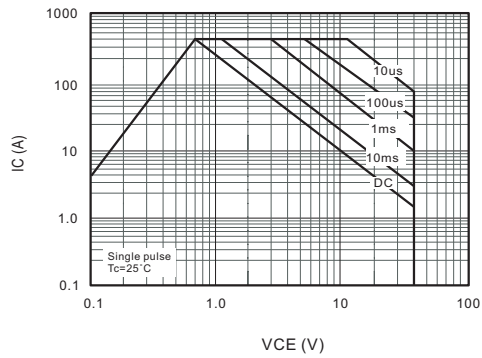


Fig.8 Max. Transient Thermal Impedance-IGBT

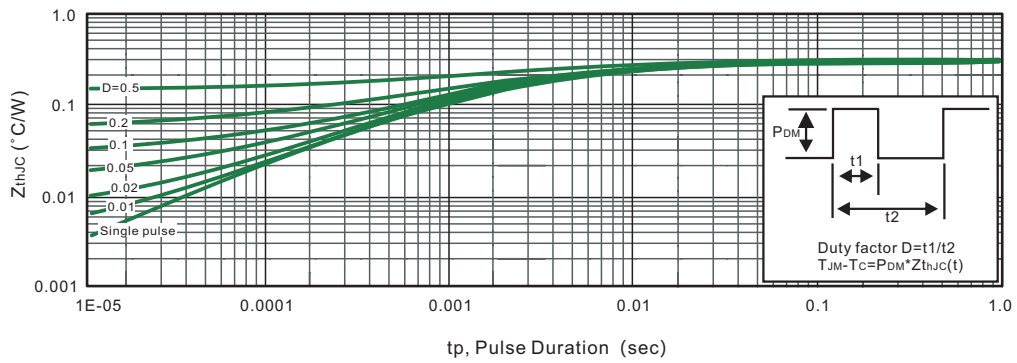
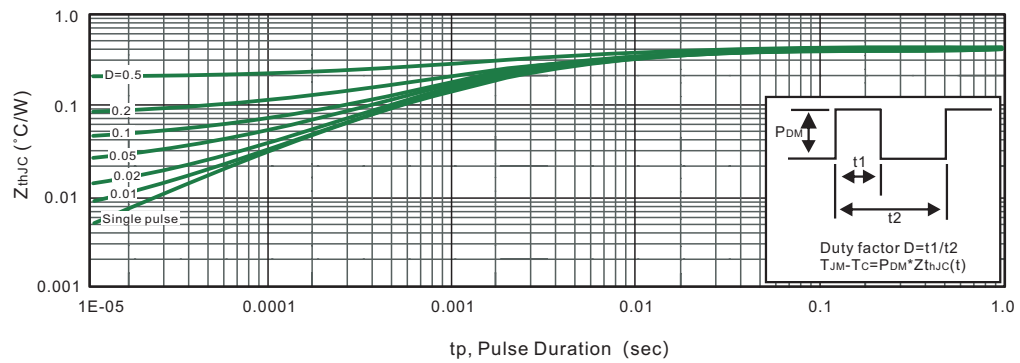


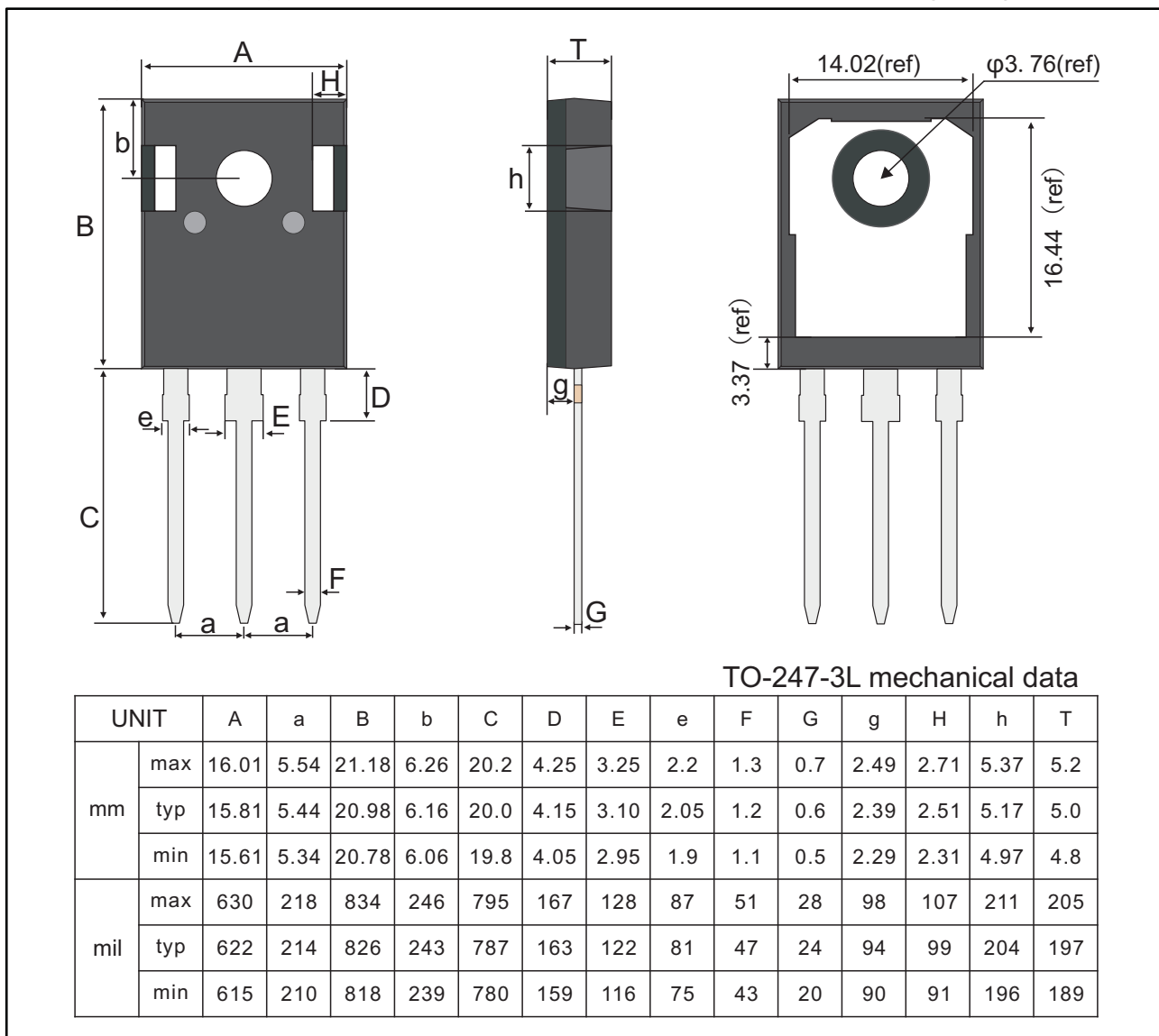
Fig.9 Max. Transient Thermal Impedance-Diode





Package Outline  
Through Hole Package ; 3 leads

TO-247-3L



Marking

Type number	Marking code
GW40NPD120SN1	GW40NPD120SN1



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